

*SYSTEM AND METHOD FOR COEVOLUTIONARY CIRCUIT DESIGN*

ABSTRACT

The present invention is directed to a system and method for coevolutionary circuit design. A system suitable for providing integrated circuit design may include a memory suitable for storing a first set of instructions and a second set of instructions and a processor communicatively coupled to the memory. The processor is suitable for performing the first set of instructions and the second set of instructions. The first set of instructions is suitable for configuring a processor to provide an integrated circuit development environment in which a support methodology for an integrated circuit is created. The second set of instructions is suitable for configuring a processor to provide tools for implementing a platform architecture of an integrated circuit in which the platform architecture supplies a structure of the integrated circuit. The first set of instructions and the second set of instructions are linked through at least one formalism so that at least one of an action taken utilizing the platform architecture influences the support methodology and an action taken utilizing the support methodology influences the platform architecture.